

Application No.: 10/054,241

Docket No. EMC2-078AUS

**Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

Claims 1-10 cancelled

11. (previously presented) A data storage system wherein end-user data is transferred between a host computer and a bank of disk drives through an interface, such interface, comprising:
  - a memory having a memory region, such memory region having a data port;
  - a plurality of directors, at least one front-end one of the directors being in communication with the host computer and at least one rear-end one of the directors being in communication with the bank of disk drives, each one of the directors comprising:
    - a central processing unit;
    - an interface state data bus section, for carrying interface state data, such interface state data bus section being in communication with both:
      - (a) the at least one front-end one and the at least one rear-end one of the directors; and
      - (b) the data port of the memory region;
    - a plurality of end-user data busses, for carrying end-user data, each one of the plurality of end-user data busses having a first end coupled to a corresponding one of the plurality of directors and a second end coupled to the data port of the memory region; and
  - wherein the central processing units of such plurality of directors control the end-user data transfer between the host computer and the bank of disk drives through the memory via

Application No.: 10/054,241

Docket No. EMC2-078AUS

the end-user data busses in response to interface state data generated by the directors, such generated interface state data being transferred among the directors via the interface state data bus section.

12. (previously presented) The system recited in claim 11 wherein the end-users data busses are serial busses.

13. (previously presented) The system recited in claim 11 wherein the interface state data bus section includes parallel busses.

14. (previously presented) The system recited in claim 13 wherein the parallel busses are coupled to the directors in a multi-drop configuration.

15. (previously presented) The system recited in claim 13 wherein the end-user data busses are serial busses.

16. (previously presented) The system recited in claim 15 wherein the parallel busses are coupled to the directors in a multi-drop configuration.

17. (previously presented) The system recited in claim 11 including a coupling node and wherein the memory has a plurality of the memory regions and wherein each one of the end-user data buses is coupled to the plurality of memory regions selectively through coupling node.

18. (previously presented) The system recited in claim 17 wherein the coupling node includes a cross-bar switch.

Application No.: 10/054,241

Docket No. EMC2-078AUS

19. (previously presented) The system recited in claim 13 wherein the interface state data bus section includes a plurality of parallel busses, each one thereof being coupled to a one of the plurality of directors and to the memory.

20. (previously presented) A method of operating a data storage system wherein end-user data is transferred between a host computer and a bank of disk drives through an interface, such method comprising:

providing a memory having a memory region, such region having a data port;

providing a plurality of directors, each one of the directors having a central processing unit, at least one front-end one of the directors being in communication with the host computer and at least one rear-end one of the directors being in communication with the bank of disk drives;

providing a plurality of interface state data busses for carrying interface state data, such interface state data busses being in communication with both:

(a) the at least one front-end one and the at least one rear-end one of the directors; and

(b) the data port of the memory region;

providing a plurality of end-user data busses, for carrying end-user data, each one of the plurality of end-user data busses having a first end coupled to a corresponding one of the plurality of directors and a second end coupled to the data port of the memory region; and

wherein the central processing units of such plurality of directors control the end-user data transfer between the host computer and the bank of disk drives through the memory via the end-user data busses in response to interface state data generated by the directors, such generated interface state data being transferred among the directors via the interface state data bus.

Application No.: 10/054,241

Docket No. EMC2-078AUS

21. (previously presented) A data storage system wherein end-user data is transferred between a host computer and a bank of disk drives through an interface, such interface, comprising:

a memory having a memory region such region having a data port;

a plurality of directors, each one having a central processing unit, at least one front-end one of the directors being in communication with the host computer and at least one rear-end one of the directors being in communication with the bank of disk drives;

an interface state data bus section, for carrying interface state data, such interface state data bus section being in communication with the at least one front-end one and the at least one rear-end one of the directors and the data port of the memory region ;

a plurality of end-user data busses, for carrying end-user data, each one of the plurality of end-user data busses having a first end coupled to a corresponding one of the plurality of directors and a second end coupled to the data port of the memory region; and

wherein such central processing units of the plurality of directors control the end-user data transfer between the host computer and the bank of disk drives via the end-user data busses in response to interface state data generated by the directors, such generated interface state data being transferred among the directors via the interface state data bus section.

22. (previously presented) The system recited in claim 21 wherein the end-users data busses are serial busses.

23. (previously presented) The system recited in claim 21 wherein the interface state data bus section includes parallel busses.

Application No.: 10/054,241

Docket No. EMC2-078AUS

24. (previously presented) The system recited in claim 23 wherein the parallel busses are coupled to the directors in a multi-drop configuration.
25. (previously presented) The system recited in claim 23 wherein the end-user data busses are serial busses.
26. (previously presented) The system recited in claim 25 wherein the parallel busses are coupled to the directors in a multi-drop configuration.
27. (previously presented) The system recited in claim 21 including a coupling node and wherein the memory has a plurality of the memory regions and wherein each one of the end-user data buses is coupled to the plurality of memory regions selectively through coupling node.
28. (previously presented) The system recited in claim 23 wherein the interface state data bus section includes a plurality of parallel busses, each one thereof being coupled to a one of the plurality of directors and to the memory.
29. (previously presented) A method of operating a data storage system wherein end-user data is transferred between a host computer and a bank of disk drives through an interface, such method comprising:
  - providing a memory having a memory region, such memory region having a data port;
  - providing a plurality of directors each one of the directors having a central processing unit, at least one front-end one of the directors being in communication with the

Application No.: 10/054,241

Docket No. EMC2-078AUS

host computer and at least one rear-end one of the directors being in communication with the bank of disk drives;

providing an interface state data section for carrying interface state data, such interface state data section being in communication with the at least one front-end one and the at least one rear-end one of the directors and the data port of the memory region;

providing a plurality of end-user data busses, for carrying end-user data, each one of the plurality of end-user data busses having a first end coupled to a corresponding one of the plurality of directors and a second end coupled to the data port of the memory region; and

wherein such central processing units of the plurality of directors control the end-user data transfer between the host computer and the bank of disk drives and the memory via the end-user data busses in response to interface state data generated by the directors, such generated interface state data being transferred among the directors.

30. (previously presented) A data storage system wherein end-user data is transferred between a host computer and a bank of disk drives through an interface, such interface, comprising:

a memory having a memory region, such memory region having a data port;

a plurality of directors, each one thereof having a central processing unit, a front-end portion of such plurality of directors being in communication with the host computer, and a rear end portion of such plurality of directors being in communication with the bank of disk drives;

an interface state data bus section, for carrying interface state data, such interface state data bus section being in communication with both the front-end portion of the plurality of directors and the rear end portion of the plurality of directors and the data port of the memory region;

Application No.: 10/054,241

Docket No. EMC2-078AUS

a plurality of end-user data busses, for carrying end-user data, each one of the plurality of end-user data busses having a first end coupled to a corresponding one of the plurality of directors and a second end coupled to the data port of the memory region; and

wherein the central processing units of such plurality of directors control the end-user data transfer between the host computer and the bank of disk drives through the memory via the end-user data busses in response to interface state data generated by the directors, such generated interface state data being transferred among the directors.

31. (previously presented) The system recited in claim 30 wherein the end-users data busses are serial busses.

32. (previously presented) The system recited in claim 30 wherein the interface state data bus section includes parallel busses.

33. (previously presented) The system recited in claim 32 wherein the parallel busses are coupled to the directors in a multi-drop configuration.

34. (previously presented) The system recited in claim 32 wherein the end-user data busses are serial busses.

35. (previously presented) The system recited in claim 34 wherein the parallel busses are coupled to the directors in a multi-drop configuration.

Application No.: 10/054,241

Docket No. EMC2-078AUS

36. (previously presented) The system recited in claim 30 including a coupling node and wherein the memory has a plurality of the memory regions and wherein each one of the end-user data buses is coupled to the plurality of regions selectively through coupling node.

37. (previously presented) The system recited in claim 36 wherein the coupling node includes a cross-bar switch.

38. (previously presented) The system recited in claim 32 wherein the interface state data bus section includes a plurality of parallel busses, each one thereof being coupled to a one of the plurality of directors and to the memory.

39. (previously presented) A method of operating a data storage system wherein end-user data is transferred between a host computer and a bank of disk drives through an interface, such method comprising:

providing a memory having a memory region, such memory region having a data port;

providing a plurality of directors, each one of the directors having a central processing unit, a front end portion of the directors being in communication with the host computer and a rear end portion of the directors being in communication with the bank of disk drives;

providing an interface state data section for carrying interface state data, such interface state data section being in communication with the front end portion of the directors and the rear end portion of the directors and the data port of the memory region;

providing a plurality of end-user data busses, for carrying end-user data, each one of the plurality of end-user data busses having a first end coupled to a corresponding one of the plurality of directors and a second end coupled to the data port of the memory region; and

Application No.: 10/054,241

Docket No. EMC2-078AUS

wherein the central processing units of such plurality of directors control the end-user data transfer between the host computer and the bank of disk drives and the memory via the end-user data busses in response to interface state data generated by the directors, such generated interface state data being transferred among the directors.

40. (previously presented) A data storage system wherein end-user data is transferred between a host computer and a bank of disk drives through an interface, such interface, comprising:

a memory having a memory region, such memory region having a data port;  
a plurality of directors, comprising:

a plurality of front-end directors, each one of such front-end directors having a central processing unit, such plurality of front end directors being in communication with the host computer, and

a plurality of rear end directors, each one of the rear-end directors having a central processing unit, such plurality of rear end directors being in communication with the bank of disk drives;

an interface state data bus section, for carrying interface state data, such interface state data bus section being in communication with both the front-end portion of the plurality of directors and the rear end portion of the plurality of directors and the data port of the memory region;

a plurality of end-user data busses, for carrying end-user data, each one of a first portion of the plurality of end-user data busses having a first end coupled to a corresponding one of the plurality of front end directors and a second end coupled to the memory and each one of a second portion of the plurality of end user data buses having a first end coupled to a corresponding one of the plurality of rear end directors and a second end coupled to the data port of the memory region; and

wherein the central processing units of such plurality of directors control the end-

Application No.: 10/054,241

Docket No. EMC2-078AUS

user data transfer between the host computer and the bank of disk drives through the memory via the end-user data busses in response to interface state data generated by the directors, such generated interface state data being transferred among the directors.

41. (previously presented) The system recited in claim 40 wherein the first portion of the end-users data busses comprises a plurality of serial busses.

42. (previously presented) The system recited in claim 40 wherein the second portion of the interface state data bus section comprises a plurality of serial busses.

43. (previously presented) The system recited in claim 42 wherein the first portion of the end-users data busses comprises a plurality of serial busses.

44. (previously presented) A method of operating a data storage system wherein end-user data is transferred between a host computer and a bank of disk drives through an interface, such method comprising:

providing a memory having a memory region with a data port;

providing a plurality of directors, such plurality of directors comprising:

a plurality of front end directors, each one thereof having a central processing unit, such plurality of front end directors being in communication with the host computer, and;

a plurality of rear end directors, each one thereof having a central processing unit, such plurality of rear end directors being in communication with the bank of disk drives;

providing an interface state data section for carrying interface state data, such interface state data section being in communication with the plurality of front end directors and the plurality of rear end directors and the data port of the memory region;

providing a plurality of end-user data busses, for carrying end-user data, each one of a first portion of the plurality of end-user data busses having a first end coupled to a

Application No.: 10/054,241

Docket No. EMC2-078AUS

corresponding one of the plurality of front end directors and a second end coupled to the memory and each one of a second portion of the plurality of end user buses having a first end coupled to a corresponding one of the plurality of the rear end directors and a second end coupled to the data port of the memory region;

wherein the central processing units of such plurality of directors control the end-user data transfer between the host computer and the bank of disk drives and the memory via the end-user data busses in response to interface state data generated by the directors, such generated interface state data being transferred among the directors.

45. (previously presented) The method recited in claim 44 wherein the first portion of the end-users data busses is provided with a plurality of serial busses.

46. (previously presented) The method recited in claim 44 wherein the second portion of the interface state data bus section is provided with a plurality of serial busses.

47. (previously presented) The method recited in claim 46 wherein the first portion of the end-users data busses is provided with a plurality of serial busses.

48. (previously presented) A system, comprising:

a plurality of directors, each one of the directors having an end user data port and an interface state data port;

a cache memory having a memory region, such memory region having a data port coupled to the end user data port and the interface state data port of one of the directors;

wherein the directors control end user data transfer with end user data in such end user data transfer passing through the cache memory in response to interface state data passing through the interface state data ports of the directors.

Application No.: 10/054,241

Docket No. EMC2-078AUS

49. (previously presented) A data storage system for transferring data between a host computer and a bank of disk drives through a system interface, such system interface comprising:

a plurality of first directors coupled to host computer, each one of the first directors having an end user data port and an interface state data port;

a plurality of second directors coupled to the bank of disk drives, each one of the second directors having an end user data port and an interface state data port;

a cache memory having a memory region, such memory region having a data port coupled to the an interface state data port and the end user data of one of the first directorsport;

wherein the first and second directors control end user data transfer between the host computer and the bank of disk drives with end user data in such end user data transfer passing through the cache memory in response to interface state data passing between the first director and the second director through the interface state data ports of the plurality of first directors and the plurality of second directors.

50. (previously presented) A system, comprising:

a plurality of directors, each one of the directors having an end user data port and an interface state data port;

a cache memory having a memory region, such region having a data port coupled to the end user data port and the interface state data port of one of the directors;

wherein the directors control end user data transfer with end user data in such end user data transfer passing to the cache memory through the end user data ports in response to interface state data passing through the interface state data ports of the directors.

51. (previously presented) A data storage system for transferring data between a host computer and a bank of disk drives through a system interface, such system interface comprising:

a plurality of first directors coupled to host computer, each one of the first directors having an end user data port and an interface state data port;

Application No.: 10/054,241

Docket No. EMC2-078AUS

a plurality of second directors coupled to the bank of disk drives, each one of the second directors having an end user data port and an interface state data port;

a cache memory having a memory region, such region having a data port coupled to the end user data port and the interface state data port of one of the directors;

wherein the first and second directors control end user data transfer between the host computer and the bank of disk drives with end user data in such end user data transfer passing through the end user data ports in response to interface state data passing between the first director and the second director through the interface state data ports of the plurality of first directors and the plurality of second directors.

52. (previously presented) A system, comprising:

a plurality of directors;

a cache memory in communication with the plurality of directors, such memory having a memory region, such memory region having a data port,

wherein the directors control end user data transfer with end user data in such data transfer passing to the cache memory through an end user data communication channel in response to interface state data passing through the directors through a different, interface state data communication channel; and

wherein the data port is coupled to the plurality of directors through the end user data communication channel and through the interface state data communication channel.

53. (previously presented) A data storage system for transferring data between a host computer and a bank of disk drives through a system interface, such system interface comprising:

a plurality of first directors coupled to host computer;

a plurality of second directors coupled to the bank of disk drives;

a cache memory in communication with the plurality of directors, such memory having a memory region, such memory region having a data port,

wherein the first and second directors control end user data transfer between the host computer and the bank of disk drives with end user data in such end user data transfer passing

Application No.: 10/054,241

Docket No. EMC2-078AUS

through an end user communication channel in response to interface state data passing between the first director and the second director through a different, interface state data communication path; and

wherein the data port is coupled to the plurality of directors through the end user data communication channel and through the interface state data communication channel.

54. (previously presented) A system, comprising:

a plurality of directors, each one of the directors having an end user data port and an interface state data port, one of such ports being coupled to a crossbar switch;

a cache memory coupled to the end user data ports of the plurality of directors having a memory region, such region having a data port coupled to the end user data port and the interface state data port of one of the directors;

wherein the directors control end user data transfer with end user data in such end user data transfer passing through the cache memory in response to interface state data passing through the interface state data ports of the directors.

55. (previously presented) A data storage system for transferring data between a host computer and a bank of disk drives through a system interface, such system interface comprising:

a plurality of first directors coupled to host computer, each one of the first directors having an end user data port and an interface state data port; one of such ports being coupled to a crossbar switch

a plurality of second directors coupled to the bank of disk drives, each one of the second directors having an end user data port and an interface state data port, one of such ports being coupled to a crossbar switch;

a cache memory coupled to the end user data ports of the plurality of first directors and second directors having a memory region, such region having a data port coupled to the end user data port and the interface state data port of one of the directors;

wherein the first and second directors control end user data transfer between the host computer and the bank of disk drives with end user data in such end user data transfer passing

Application No.: 10/054,241

Docket No. EMC2-078AUS

through the cache memory in response to interface state data passing between the first director and the second director through the interface state data ports of the plurality of first directors and the plurality of second directors.

56. (currently amended) A system, comprising:

a plurality of directors, each one of the directors having an end user data port and an interface state data port, one of such ports being coupled to a crossbar switch;

a cache memory having a memory region, such region having a data port coupled to the end user data port and the interface state data port of one of the directors;

wherein the directors control end user data transfer with end user data in such end user data transfer passing to the cache memory through the end user data ports in response to interface state data passing through the interface state data ports of the directors.

57. (previously presented) A data storage system for transferring data between a host computer and a bank of disk drives through a system interface, such system interface comprising:

a plurality of first directors coupled to host computer, each one of the first directors having an end user data port and an interface state data port, one of such ports being coupled to a crossbar switch;

a plurality of second directors coupled to the bank of disk drives, each one of the second directors having an end user data port and an interface state data port, one of such ports being coupled to a crossbar switch;

a cache memory having a memory region, such region having a data port coupled to the end user data port and the interface state data port of one of the directors;

wherein the first and second directors control end user data transfer between the host computer and the bank of disk drives with end user data in such end user data transfer passing through the end user data ports in response to interface state data passing between the first director and the second director through the interface state data ports of the plurality of first directors and the plurality of second directors.

Application No.: 10/054,241

Docket No. EMC2-078AUS

58. (previously presented) A system, comprising:

a plurality of directors;

a cache memory in communication with the plurality of directors, such memory having a memory region, such memory region having a data port,

wherein the directors control end user data transfer with end user data in such data transfer passing to the cache memory through an end user data communication channel in response to interface state data passing through the directors through a different, interface state data communication channel, one of such channels being coupled to a crossbar switch; and

wherein the data port is coupled to the plurality of directors through the end user data communication channel and through the interface state data communication channel.

59. (previously presented) A data storage system for transferring data between a host computer and a bank of disk drives through a system interface, such system interface comprising:

a plurality of first directors coupled to host computer;

a plurality of second directors coupled to the bank of disk drives;

a cache memory in communication with the plurality of directors, such memory having a memory region, such memory region having a data port,

wherein the first and second directors control end user data transfer between the host computer and the bank of disk drives with end user data in such end user data transfer passing through an end user communication channel in response to interface state data passing between the first director and the second director through a different, interface state data communication channel one of such channels being coupled to a crossbar switch; and

wherein the data port is coupled to the plurality of directors through the end user data communication channel and through the interface state data communication channel.